

FIG. 1

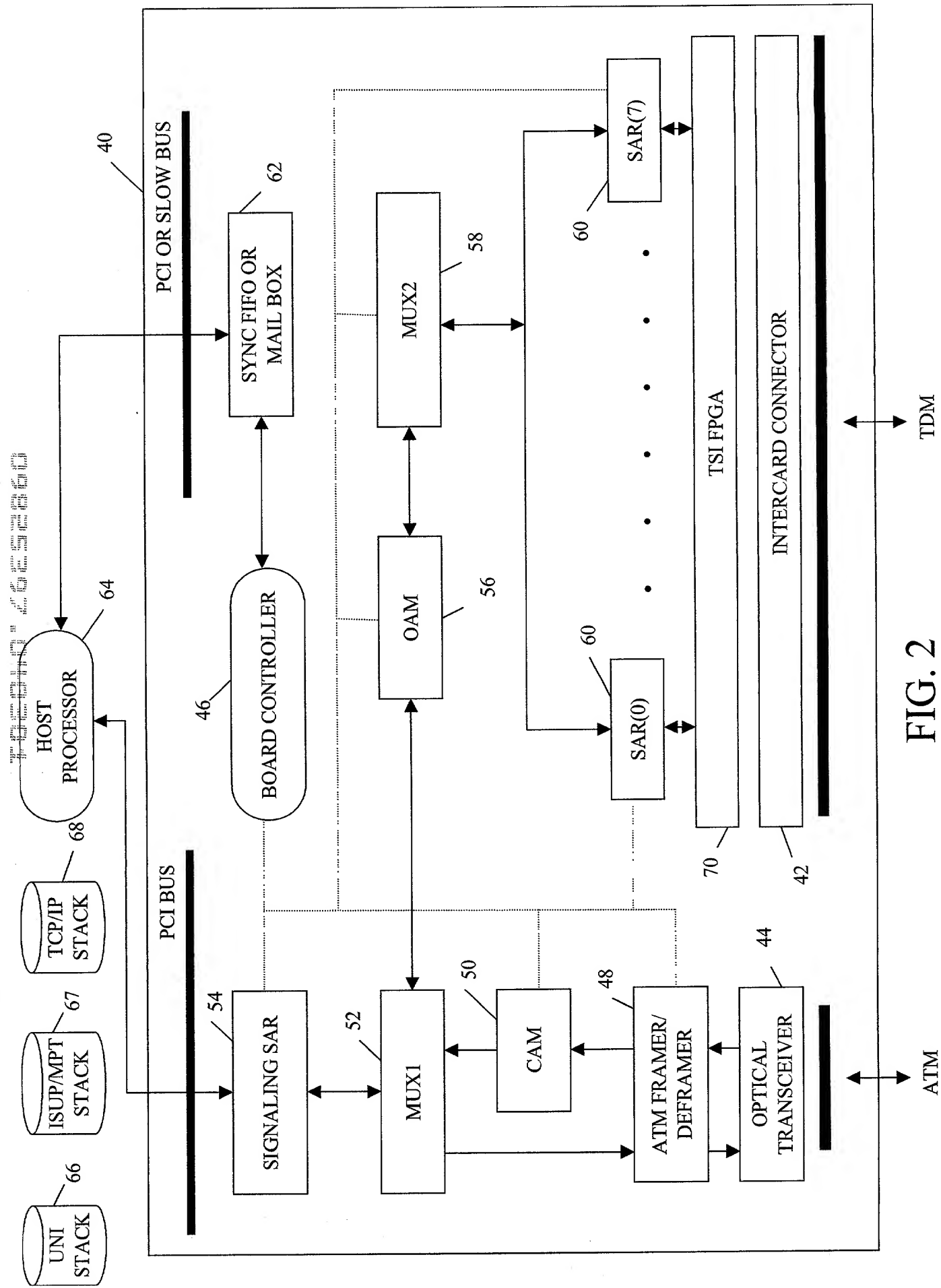


FIG. 2

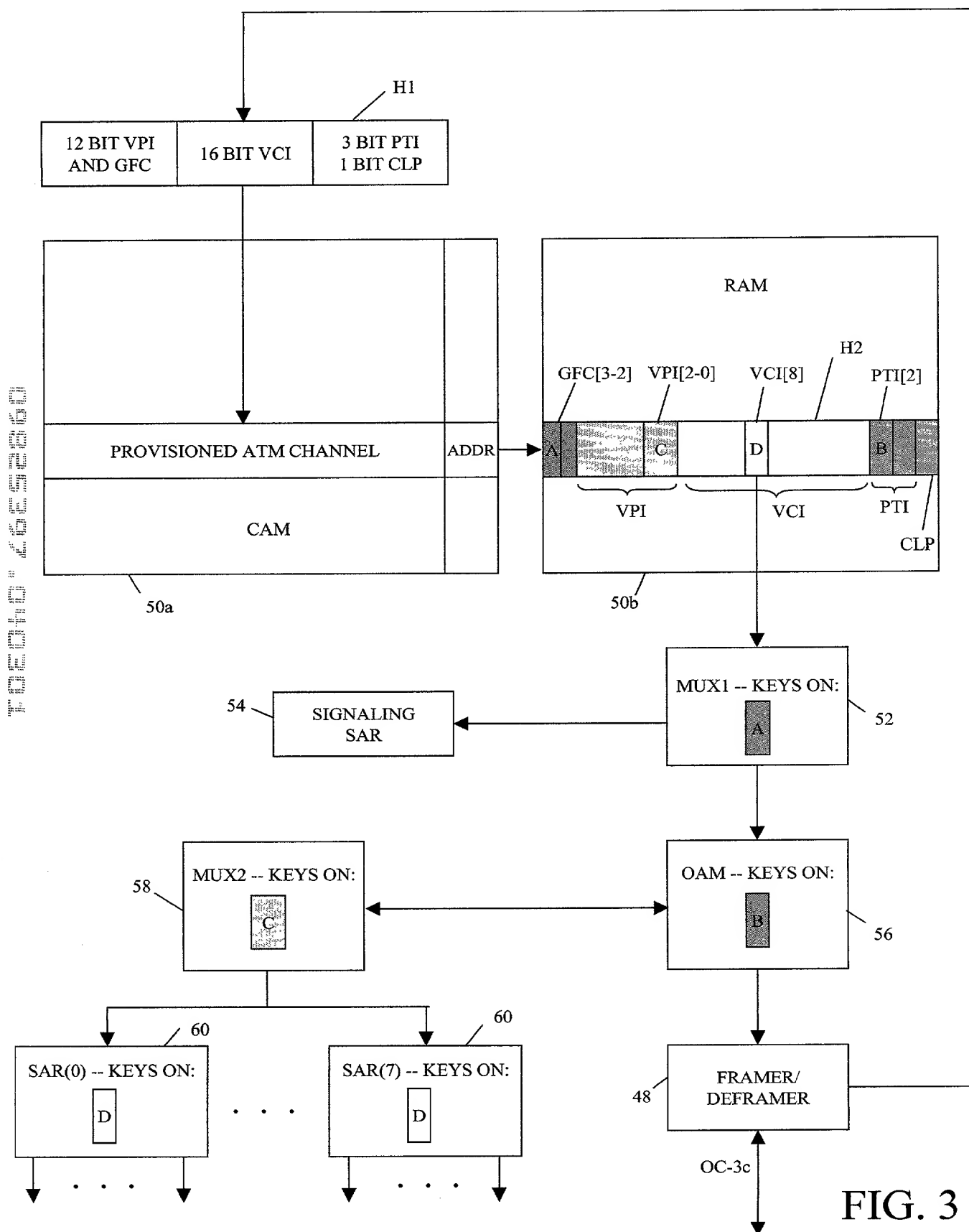


FIG. 3

# CAM/AM CELL HEADER TRANSLATION

HEADER FIELD	GFC 4-BIT		VPI 8-BIT		VCI 16-BIT			PTI 3-BIT	CL P	MUX1	OAM	MUX2	SAR
BIT FIELD POSITION	3-2 (A)	1-0	7-3	2-0 (C)	15-9 (D)	7-0	2 1-0 (B)	0		KEY ON GFC[3-2]	KEY ON PTI[2]	KEY ON VPI[2-0]	KEY ON VCI[8]
AAL1 USER CELLS	0	X	X	ANY	X	ANY	0	X	X	1-TO-4 DEMUX TO OAM	USER CELLS 0 PASS-THROUGH	1-TO-8 DEMUX	256 DS0 CHANNELS
SEG/END F5 OAM CELLS	0	X	X	=USER	X	SAME AS = USER	1	X	X	SAME AS ABOVE	OAM F5 CELLS ARE TERMINATED	N/A	N/A
SEG/END F4 OAM CELLS	0	X	ANY	ANY	X	SEG=3, END-TO-END=4	1	X	X	SAME AS ABOVE	OAM VCI=3 OR 4 ARE TERMINATED	N/A	N/A
SIGNALING HAIRPIN	0	X	ANY	ANY	X	ANY	0	X	X	SAME AS ABOVE	PASS THROUGH	1-TO-8 DEMUX	OAM QUEUE
UNHANDLED F5 OAM CELLS	0	X	X	=USER	0	SAME AS = USER	0	X	X	SAME AS ABOVE	PASS THROUGH	1-TO-8 DEMUX	OAM QUEUE
UNHANDLED F4 OAM CELLS	0	X	ANY	ANY	X	SEG=3, END-TO-END=4	0	X	X	SAME AS ABOVE	PASS THROUGH	1-TO-8 DEMUX	OAM QUEUE
AAL5 DATA CELLS	1	ANY	ANY	ANY	ANY	ANY	0	X	X	1-TO-4 DEMUX TO SIGNALING SAR	N/A	N/A	N/A
AAL5 F5 OAM CELLS	1	SAME AS AAL5 DATA CELLS							X	SAME AS ABOVE	N/A	N/A	N/A
AAL5 F4 OAM CELLS	1	ANY	ANY	ANY	ANY	SEG=3, END-TO-END=4	0	X	X	SAME AS ABOVE	N/A	N/A	N/A

FIG. 4

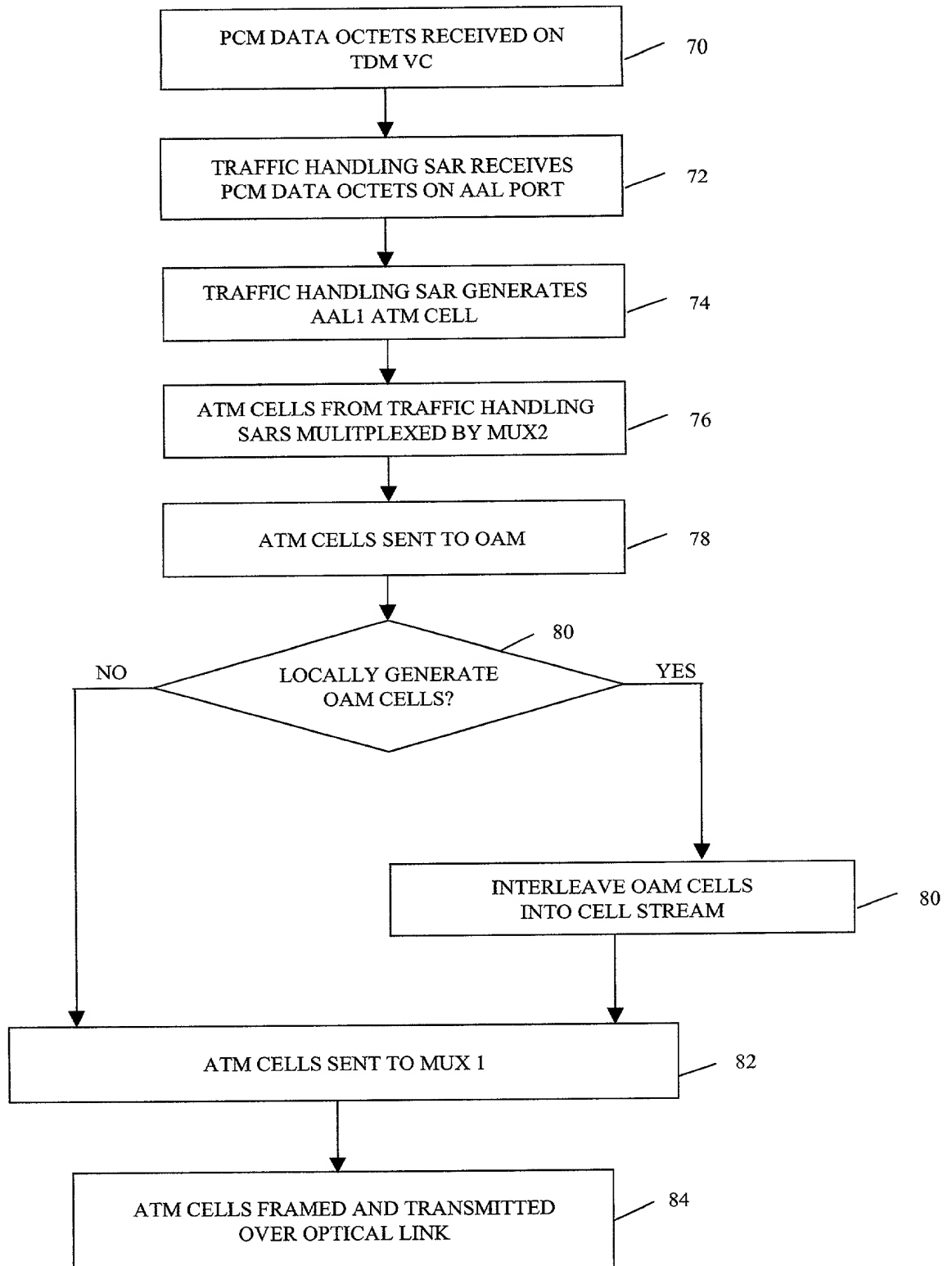


FIG. 5

Patent Application No. 2003/0126360

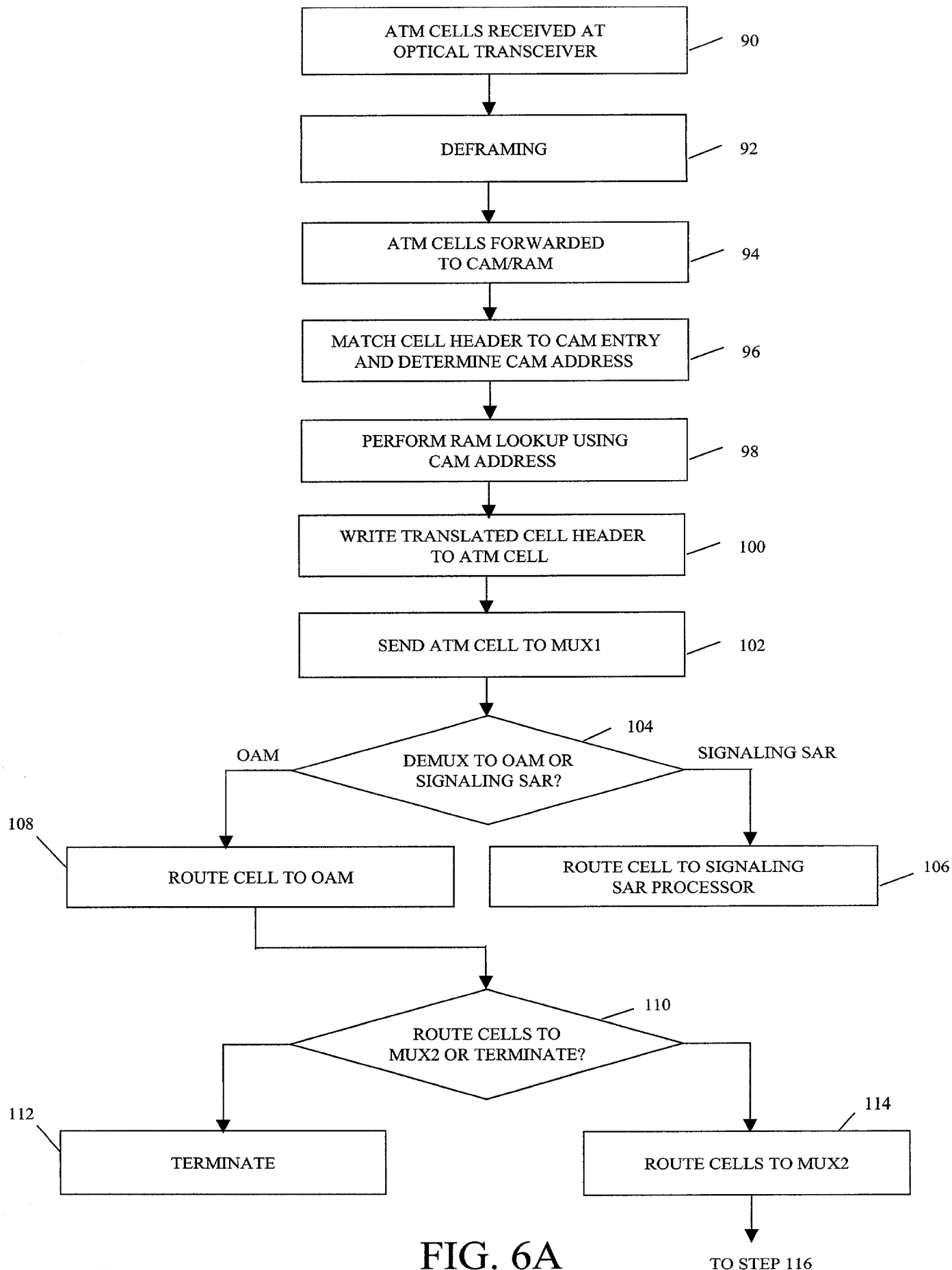


FIG. 6A

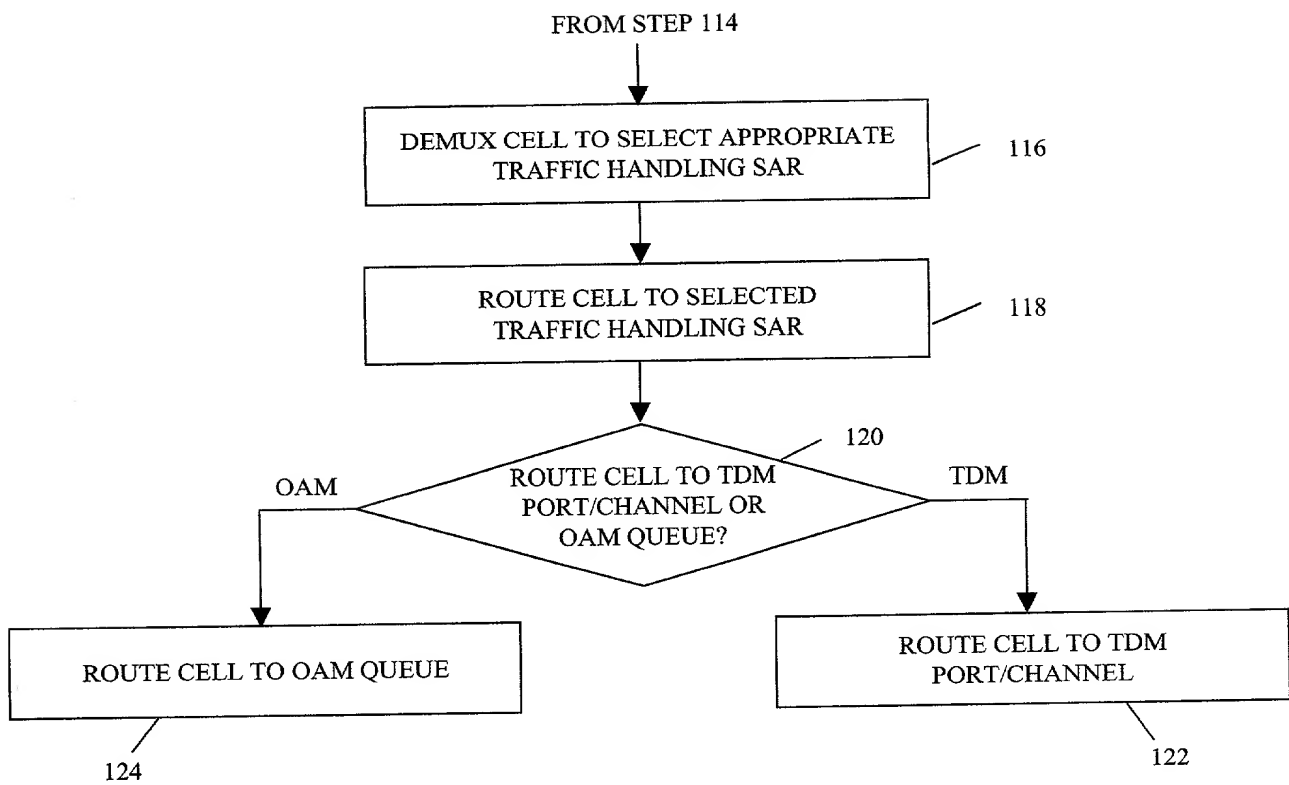


FIG. 6B